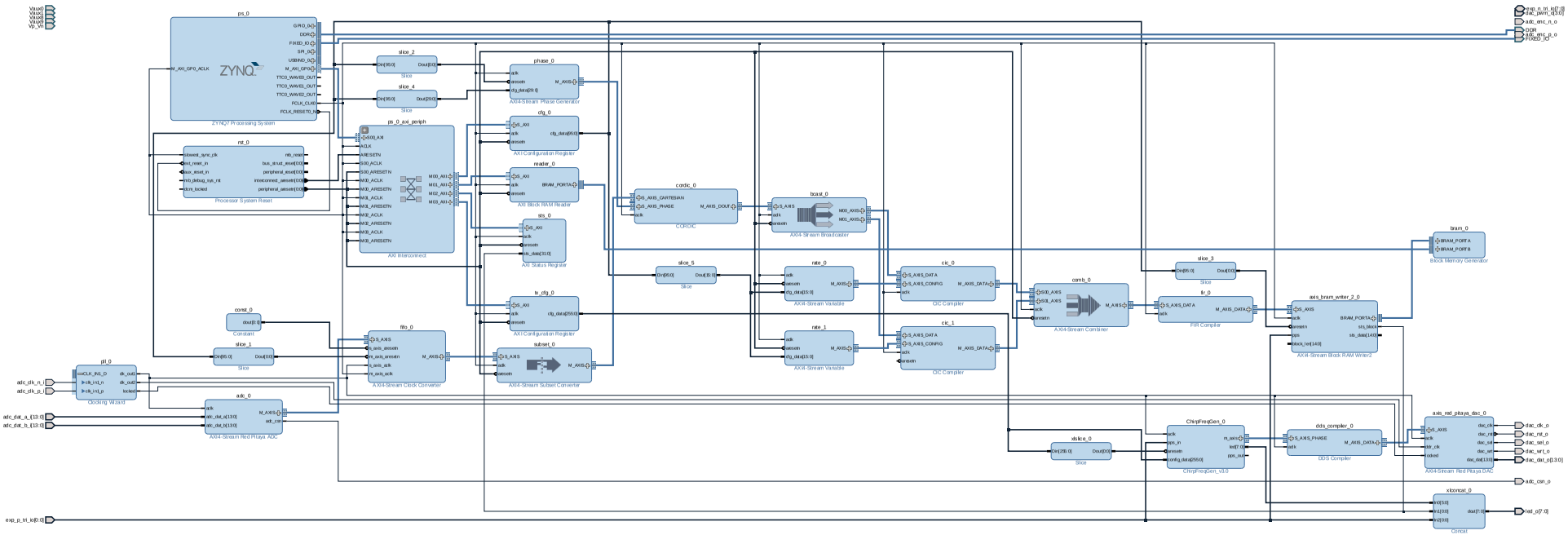
Vaux0
Vaux1
Vaux8
Vaux9
Vp_Vn
ps_0
ZYNQ
ZYNQ7 Processing System
M_AXI_GP0_ACLK
GPIO_0
DDR
FIXED_IO
S_AXI_GP0
USBIND_0
M_AXI_GP0
TTC0_WAVE0_OUT
TTC0_WAVE1_OUT
TTC0_WAVE2_OUT
FCLK_CLK0
FCLK_RESET0_N
exp_p_tri_io[7:0]
dac_pwm_d[13:0]
adc_enc_n_o
DDR
FIXED_IO
slice_2
Slice
slice_4
Slice
phase_0
AXI4-Stream Phase Generator
cfg_0
AXI Configuration Register
reader_0
AXI Block RAM Reader
sts_0
AXI Status Register
tx_cfg_0
AXI Configuration Register
subset_0
AXI4-Stream Subset Converter
ps_0_axi_periph
AXI Interconnect
rst_0
Processor System Reset
const_0
Constant
slice_1
Slice
fifo_0
AXI4-Stream Clock Converter
prl_0
Clocking Wizard
adc_clk_n_i
adc_clk_p_i
adc_0
AXI4-Stream Red Pitaya ADC
adc_dat_a_i[13:0]
adc_dat_b_i[13:0]
exp_p_tri_io[7:0]
cordic_0
CORDIC
bcast_0
AXI4-Stream Broadcaster
slice_5
Slice
nco_0
AXI4-Stream Variable
nco_1
AXI4-Stream Variable
cic_0
CIC Compiler
cic_1
CIC Compiler
comb_0
AXI4-Stream Combiner
slice_3
Slice
fir_0
FIR Compiler
bram_0
Block Memory Generator
axis_bram_writer_2_0
AXI4-Stream Block RAM Writer2
xslice_0
Slice
CHirpFreqGen_0
CHirpFreqGen_v3.0
dds_compiler_0
DDS Compiler
axis_red_pitaya_dac_0
AXI4-Stream Red Pitaya DAC
dac_clk_o
dac_rst_o
dac_sel_o
dac_wrt_o
dac_dat_o[13:0]
adc_csn_o
xlconcat_0
Concat
xad_o[7:0]